

FIG. 1

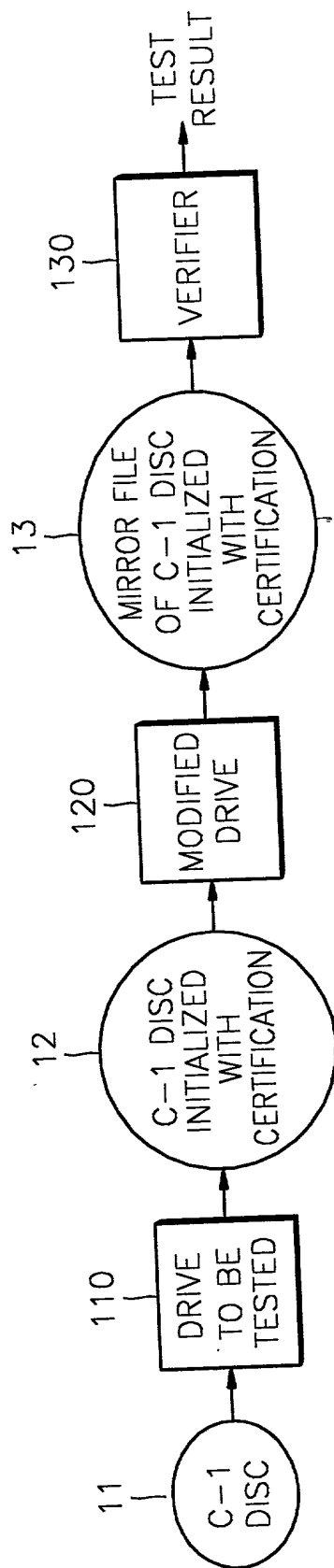


FIG. 2

Class	Items		Byte Position	of bytes	Expected value
DMA	Error condition of DMA	DMA1	NA	NA	No uncorrectable error
		DMA2	NA	NA	No uncorrectable error
		DMA3	NA	NA	No uncorrectable error
		DMA4	NA	NA	No uncorrectable error
	DDS/PDL and SDL Update counters	DDS/PDL update counter in DDS1	4 to 7	4	M+k
		DDS/PDL update counter in SDL1	16 to 19	4	M+k
		DDS/PDL update counter in DDS2	4 to 7	4	M+k
		DDS/PDL update counter in SDL2	16 to 19	4	M+k
		DDS/PDL update counter in DDS3	4 to 7	4	M+k
		DDS/PDL update counter in SDL3	16 to 19	4	M+k
		DDS/PDL update counter in DDS4	4 to 7	4	M+k
		DDS/PDL update counter in SDL4	16 to 19	4	M+k
		SDL update counter in SDL1	4 to 7	4	N+k
		SDL update counter in SDL2	4 to 7	4	N+k
		SDL update counter in SDL3	4 to 7	4	N+k
		SDL update counter in SDL4	4 to 7	4	N+k
	Contents of DMA	DMA1	NA		Should be identical
		DMA2	NA		Should be identical
		DMA3	NA		Should be identical
		DMA4	NA		Should be identical

# FIG. 3

Class	Items		Byte Position	of bytes	Expected value	
DDS	DDS Identifier		0 to 1	2	0A0Ah	
	reserved		2	1	00h	
	Disc Certification flag		3	1	b7	0b
					b6-b2	All 0b
					b1	1b
					b0	0b
	DDS/PDL update counter		4 to 7	4	M+k	
	Number of Groups		8 to 9	2	0001h	
	Number of zones		10 to 11	2	0023h	
	reserved		12 to 79	68	All 00h	
	Location of Primary spare area		80 to 87	8	b63-b56	00h
					b55-b32	031000h
					b31-b24	00h
					b23-b0	0341FFh
	Location of LSN0		88 to 91	4	b31-b24	00h
					b23-b0	-
	reserved		92 to 255	164	All 00h	
	Start LSN for each zone	Zone1	256 to 259	140	b31-b24	00h
		Zone2	260 to 263		b23-b0	-
		...	...		b31-b24	00h
					b23-b0	-
					...	...
		Zone34	392 to 395		b31-b24	00h
					b23-b0	-
	reserved		396 to 2047	1652	All 00h	

# FIG. 4

Class	Items		Byte Position	of bytes	Expected value	
PDL	PDL Identifier		0 to 1	2	0001h	
	Number of entries in PDL		2 to 3	2	$E_{PDL}$	
	Integrity of PDL entries	First PDL entry	4 to 7	4	b31-b30	10b
					b23-b0	10b
		Second PDL entry	8 to 11	4	b31-b30	10b
					b23-b0	10b
		...	...	...	...	...
		Last PDL entry	n to n+3	4	b31-b30	10b
					b23-b0	10b
		Size of PDL			$4 \times E_{PDL} + 4$	
Un-used area		n+4 to 30719		FFh		

# FIG. 5

Class	Items		Byte Position	of bytes	Expected value	
SDL	SDL identifier		0 to 1	2	0002h	
	reserved		2 to 3	2	00h	
	SDL Update counter		4 to 7	4	N+k	
	Start sector number of SSA		8 to 11	4	b31-b24	00h
					b23-b0	-
	Total number of logical sectors		12 to 15	4		
	DDS/PDL Update counter		16 to 19	4	M+k	
	Spare area full flag		20	1	b7-b2	All 0b
					b1	-
					b0	-
	reserved		21	1	00h	
	Number of entries in SDL		22 to 23	2	E <sub>SDL</sub>	
	Integrity of SDL entries	First SDL entry	24 to 31	8	b63-b56	00h
					b55-b32	
					b23-b0	
		Second SDL entry	32 to 39	8	b62	
					b55-b32	
					b23-b0	
		...	...	...	...	...
		Last SDL entry	m to m+7	8	b62	
					b55-b32	
	b23-b0					
	Size of PDL				8xE <sub>SDL</sub> +24	
	Un-used SDL area		m+8 to 32767		FFh	

FIG. 6

